



SiFive

**Chip-to-Chip Communication (Interlaken-LL) for  
Enterprise and Cloud – Q&A**

**1. What is latency and throughput?**

*Latency is number of cycles it takes for specific data from the transmit side of source to the receive side of destination.*

*Throughput is overall data sent every cycle (measured as Gigabit/Second).*

**2. Do you have any success story from customers with silicon?**

*We have to-date 75+ licensees. Yes, we have many customers on silicon. If you share details of your use case we can share customer testimonials.*

**3. Does Interlaken supports a maximum 1.2 Tbps between two chips?**

*Yes, 1.2Tbps. Some % loss due to encoding/decoding expected. We have achieved up to 95% efficiency in some cases.*

**4. If we use multiple lanes to transfer high speed data transfer, does there will be any latency or performance drop?**

*Excluding SerDes latency, Interlaken is agnostic to how many lanes are used. It can be one lane or it can be for e.g. 16 lanes.*